



Paper #4

Sheet 1 of 1

 APPLICANT'S ART CITATION <small>PATENT & TRADEMARK OFFICE</small> (Use several sheets if necessary)	Application 09/821,444	• OFGS File No. P/2041-58
	Applicant SAKAI, J.	
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U.S. PATENT DOCUMENTS

FOREIGN PATENT DOCUMENTS

OTHER DOCUMENTS (Including Author, Title, Date, Pertinent Pages, Etc.)

		<p>Torii et al., "Control Parallel On-Chip Multi-processor: MUSCAT," JSPP '97 Joint Symposium on Parallel Processing 1997, the Information Processing Society of Japan, pp. 229-236, May, 1997. (SEE DISCUSSION IN SPECIFICATION)</p> <p><i>Abstract Only</i> <i>Document in Japanese</i></p>
		<p>Torii et al., "On-Chip Control Parallel Multi-processor: MUSCAT," IPSJ TRANSACTION, Vol. 39, No. 6, June, 1998. (SEE DISCUSSION IN SPECIFICATION)</p> <p><i>Abstract Only</i> <i>Document in Japanese</i></p>
		<p>Sakai et al., "Automatic Parallelizing Method for Control-parallel Multi-threaded Architecture," IPSJ TRANSACTION, Vol. 40, No. 5, May, 1999. (SEE DISCUSSION IN SPECIFICATION)</p> <p><i>Abstract Only</i> <i>Document in Japanese</i></p>
		<p>A. V. Aho et al., "Compilers-II: Principles, Techniques, and Tools," translated into Japanese by Kenichi Harada, SAIENSE-SHA, 1990, pp. 734-741. (SEE DISCUSSION IN SPECIFICATION)</p> <p><i>No English Version</i> <i>Not Considered</i></p>

Examiner

Date Considered

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